

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In RE Application of:

Justin K. Brask

Serial No.: 10/652,796

Filing Date: August 28, 2003

For: **METHOD FOR MAKING A SEMICONDUCTOR  
DEVICE HAVING A HIGH-K GATE DIELECTRIC**

Art Unit: 2813

Examiner: Dolan, Jennifer M.

Assistant Commissioner for Patents  
Washington, D.C. 20231  
Attn: Official Draftsperson

**SUBMISSION OF FORMAL DRAWINGS**

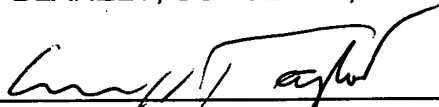
Dear Sir:

In response to the Notice of Allowability mailed 04/28/2005 enclosed herewith for filing in the above-referenced patent application are formal drawings including 1 sheet and 5 Figures. The drawing informalities have been noted and the appropriate corrections are incorporated herein.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: July 28, 2005

  
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231 on July 28, 2005 by: T.J. Delgado.

  
T.J. Delgado

  
Date